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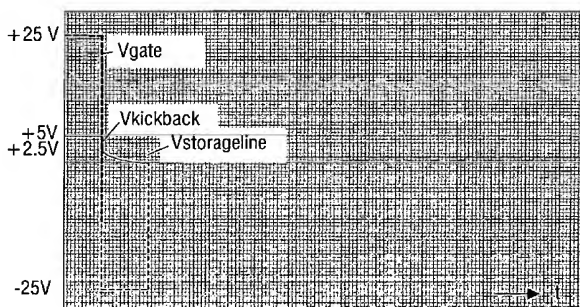
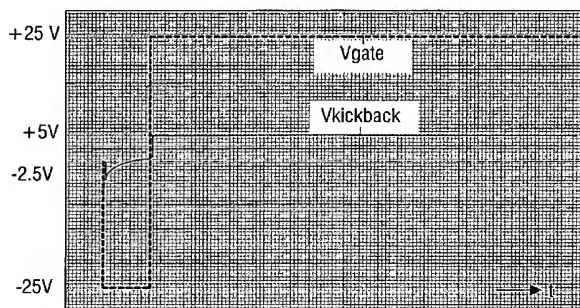
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(54) Title: DISPLAY UNIT



(57) Abstract: Display units (1) comprising pixels arranged in rows and columns coupled via transistors (12) to row (41, 45, 49) and column (31, 32, 39) electrodes show a relatively large gradient in the image. By introducing means (30, 40) for reducing a voltage difference resulting from a voltage jump on a predefined line, which voltage jump arrives via a capacitance (13, 14), this gradient is reduced. The capacitance (13, 14) may comprise a storage capacitor (13), with the predefined line being a neighbouring row electrode (41, 45, 49) or a separate storage line (25). The capacitance (13, 14) may also comprise a parasitic capacitor (14) of the transistor (12), with the predefined line corresponding with the row electrode (41, 45, 49) in the same row. The means (30, 40) comprise line driving circuitry (40) and data driving circuitry (30) for supplying a data signal to pixels (11). The means (30, 40) may also comprise line driving circuitry (40) for driving at a reduced amplitude and may comprise storage line driving circuitry for driving the storage line (25).



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